

Abstract of the Disclosure

A system and method for processing an input signal, such as an analog video input signal, are described. The system includes a correlated double sampler (CDS) for receiving an input signal, sampling the input signal and providing an output signal, the
5 CDS comprising an amplifier for amplifying the input signal. The sampled and amplified output of the CDS is applied to a programmable gain amplifier (PGA). The PGA receives the output signal from the CDS and amplifies the received signal. By providing gain in both the CDS and the PGA, the system of the invention uses much smaller area than conventional systems. Also, a pseudo log scale gain response for the overall system
10 is realized.

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